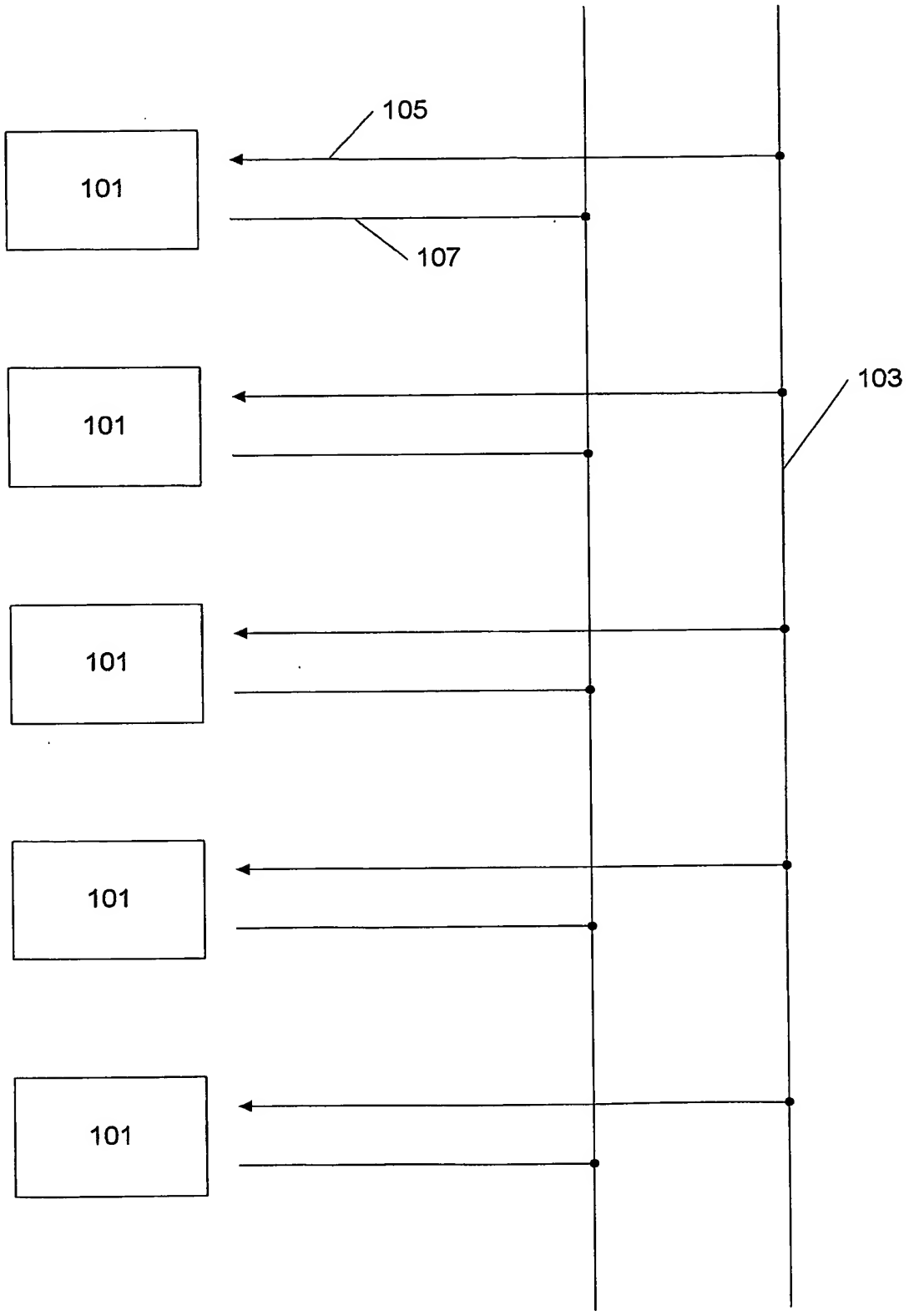

Fig. 1

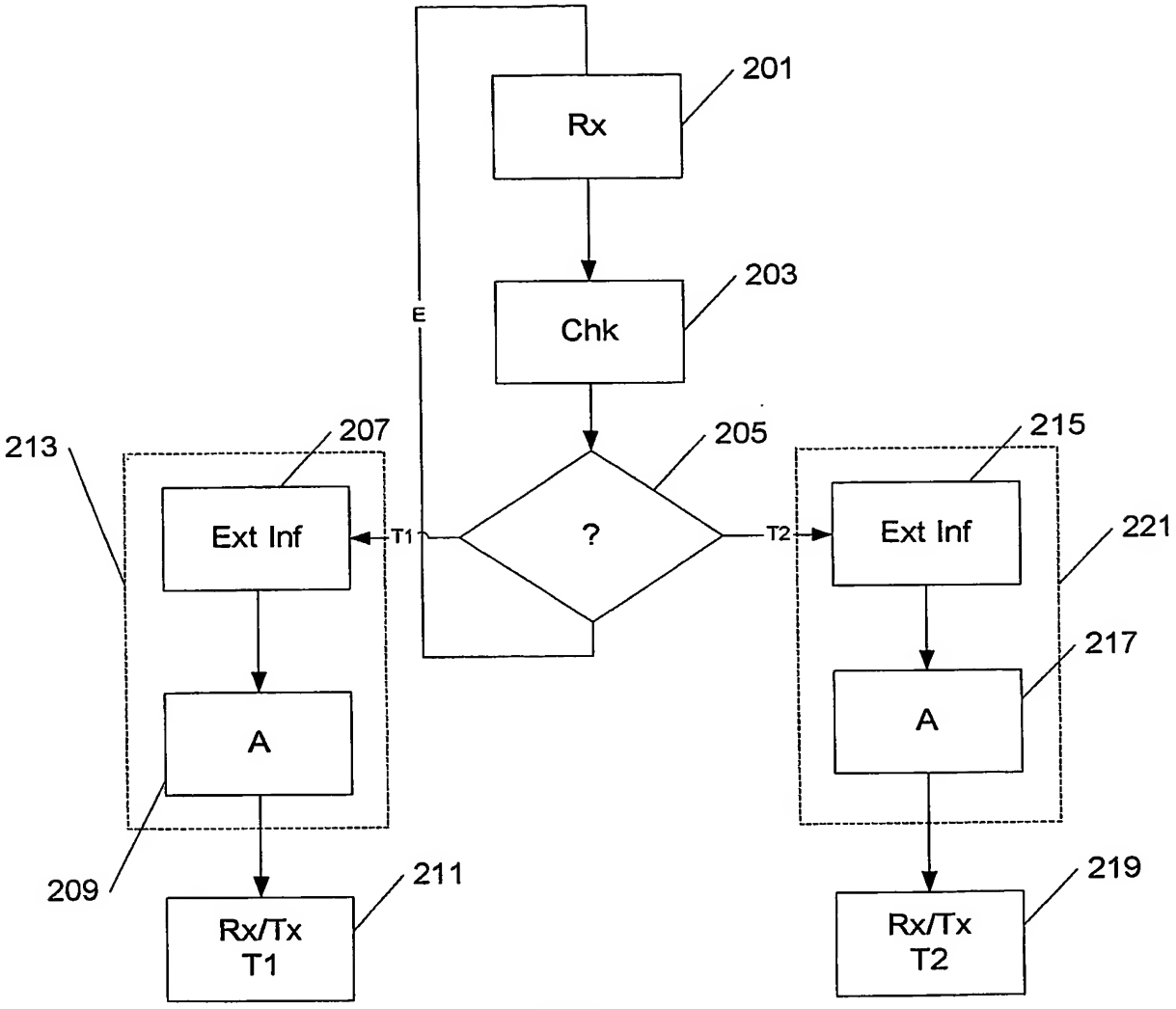
201
Rx
203
Chk
E
205
?
T1
T2
207
213
Ext Inf
A
209
211
Rx/Tx
T1
215
Ext Inf
221
217
A
219
Rx/Tx
T2

Fig. 2

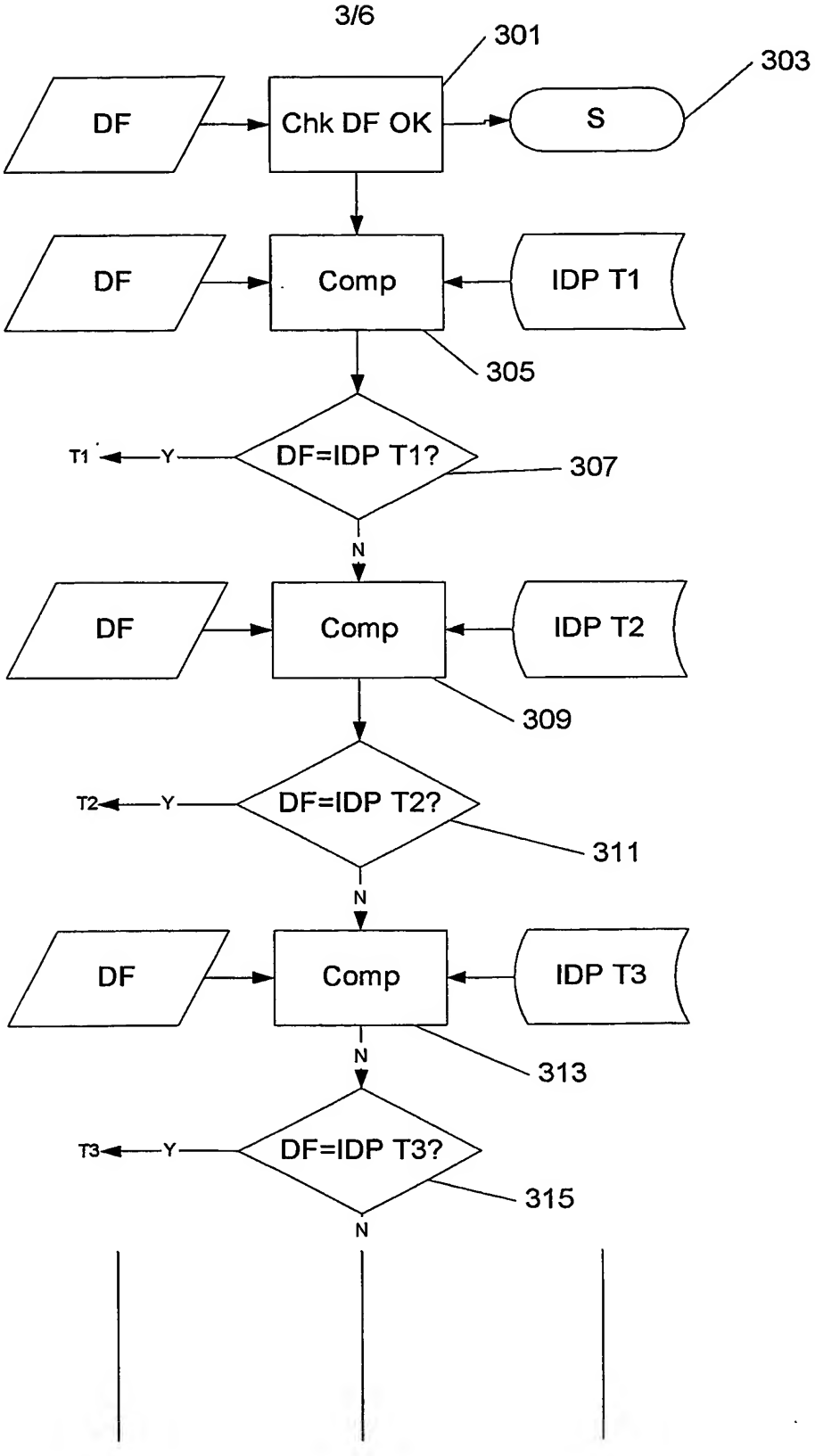
301
303
DF
Chk DF OK
S
DF
Comp
IDP T1
305
T1
Y
DF=IDP T1?
307
N
DF
Comp
IDP T2
309
T2
Y
DF=IDP T2?
311
N
DF
Comp
IDP T3
313
N
T3
Y
DF=IDP T3?
315
N

Fig. 3

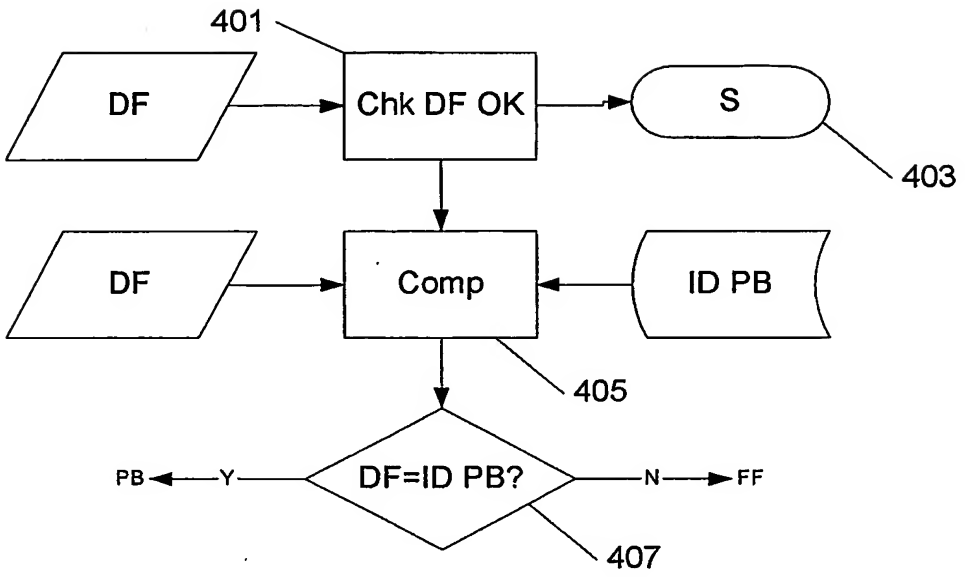

Fig. 4

DLPDU Class	FC	DL Address				
	Frame Control	Destination (DA)	Source (SA)	2nd source	Parameters	User Data
EC 1	1111 LF00	[HL.]N.S	[HL.]N.S	[HL.]N.S	EC-p	o-DLSDU
EC 2	1110 LF00	—	[HL.]N.S	[HL.]N.S	EC-p	o-DLSDU
DC 1	0111 LF00	[HL.]N.S	[HL.]N.S	—	DC-p	o-DLSDU
DC 2	0110 LF00	—	[HL.]N.S	—	DC-p	o-DLSDU
CD 1	1111 LFPP	[HL.]N.S	[HL.]N.S	—	—	—
CD 2	1011 LFPP	[HL.]N.S	—	—	—	—
DT 1	1101 LFPP	[HL.]N.S	[HL.]N.S	—	SD-p	o-DLSDU
DT 2	1001 LFPP	[HL.]N.S	—	—	SD-p	o-DLSDU
DT 3	0101 LFPP	—	[HL.]N.S	—	SD-p	o-DLSDU
DT 5	0101 0F00	—	[PDA]	—	SD-p	o-DLSDU
SR	0001 0F11	[PSA]	N	—	o-SR-p	—
CT	0001 0F00	—	—	—	—	—
TD	0001 0F01	—	N	—	TD-p	—
RQ	1100 0F00	N.0	N.0	—	RQ-p	—
RR	1101 0F00	N.0	N.0	—	RR-p	—
PN	0010 0110	N	—	—	PN-p	—
PR	0010 0111	—	—	—	—	SPDU
PT	0011 0FPP	N	—	—	DD-p	—
RT	0011 0100	—	[DTH]	—	—	—
RI	0010 0000	—	[DTH]	—	DD-p	—
CL	0000 0001	—	N	—	—	—
TL	0000 0110	N	—	—	—	SPDU
Idle	0001 0F10	—	—	—	—	o-DLSDU

Fig. 5

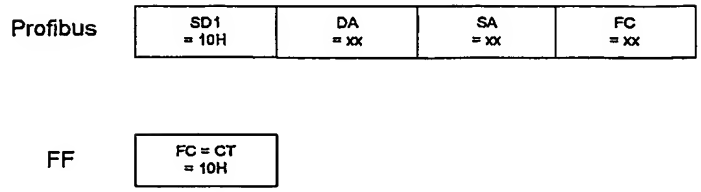

Fig. 6

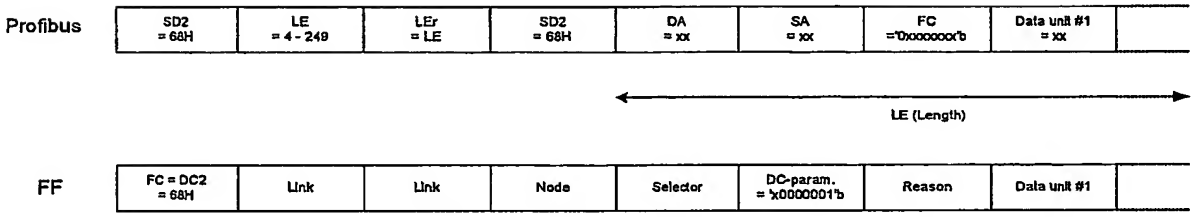

Fig. 7

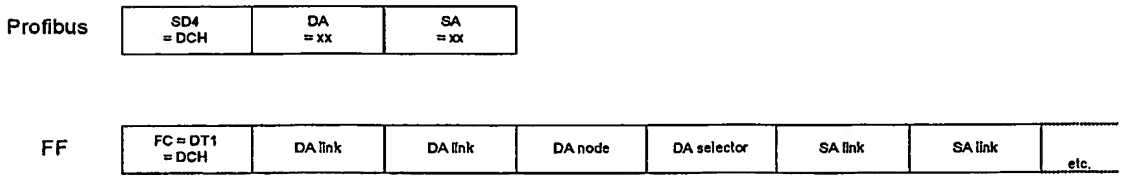

Fig. 8

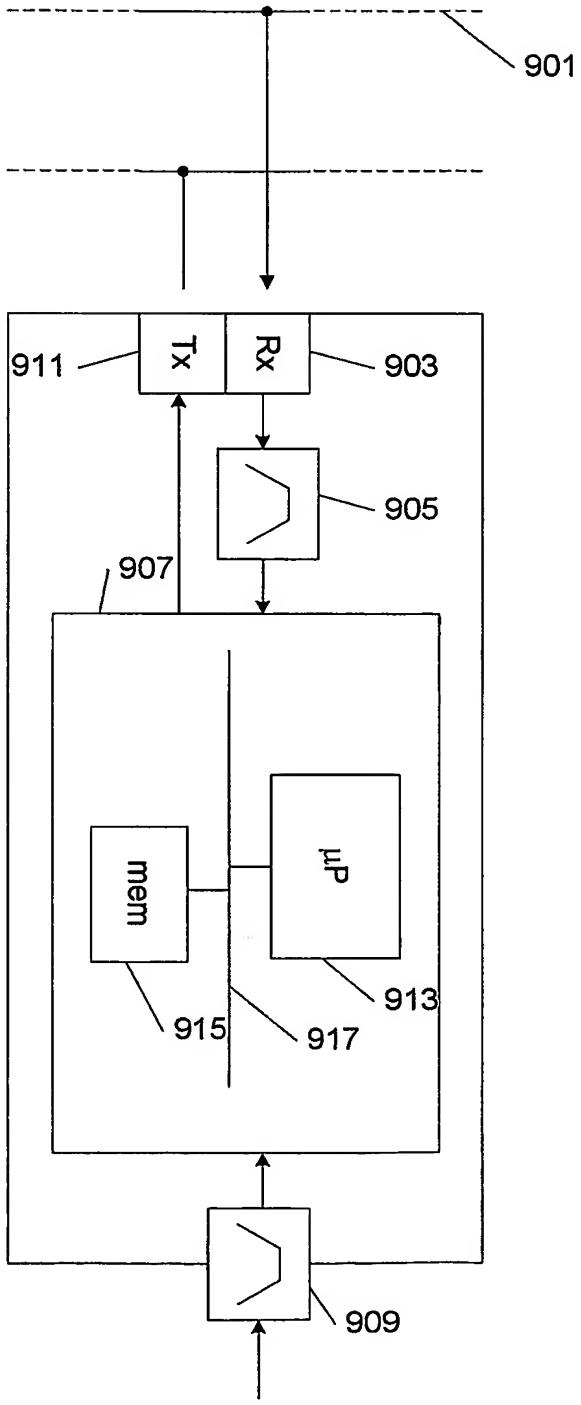

Fig. 9